Linear Voltage Regulator - SiS1084L-3.3

Positive Fixed 3.3V Output Low Dropout Voltage Regulator in bare die form

Rev 1.0
10/05/22

Description

The SiS1084L-3.3V is a positive 3.3V fixed output regulator providing 5A output current with high efficiency. The device accepts input voltages up to 12V and is optimised for smallest die size. Voltage dropout is guaranteed at 1.5V maximum at 5A. This device also features on-chip trimming for current limit + output voltage and includes thermal shutdown for rugged performance.

Features:

- 12V input capability with optimised die size
- 1.5V dropout voltage maximum at 5A
- Internal current limiting & reference trimming
- Thermal shutdown
- Line & Load Regulation: 20mV maximum
- Full military temperature range.

Ordering Information

The following part suffixes apply:

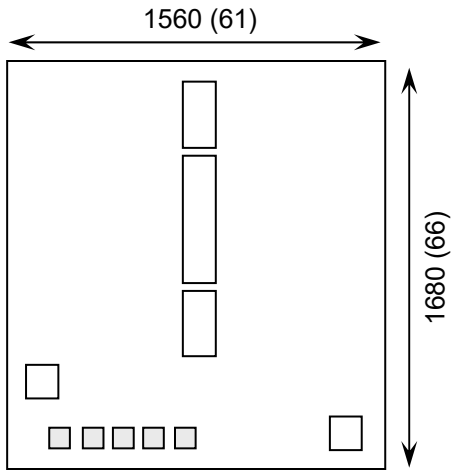
- No suffix - MIL-STD-883 /2010B Visual Inspection
- "**H**" - MIL-STD-883 /2010B Visual Inspection
  + MIL-PRF-38534 Class H LAT
- "**K**" - MIL-STD-883 /2010A Visual Inspection (Space)
  + MIL-PRF-38534 Class K LAT

LAT = Lot Acceptance Test.

For further information on LAT process flows see below.

www.siliconsupplies.com\quality\bare-die-lot-qualification

Die Dimensions in µm (mils)



Supply Formats:

- Default – Die in Waffle Pack (100 per tray capacity)
- Sawn Wafer on Tape – On request
- Un-sawn Wafer – On request
- In Metal or Ceramic package – On request

Mechanical Specification

Die Size (Unsawn)	1560 x 1680 61 x 66	µm mils
Minimum Bond Pad Size	130 x 130 5.11 x 5.11	µm mils
Die Thickness	350 (±20) 13.78 (±0.79)	µm mils
Top Metal Composition	Al 1%Si 1.4µm	
Back Metal Composition	Ti/Ni/Ag	

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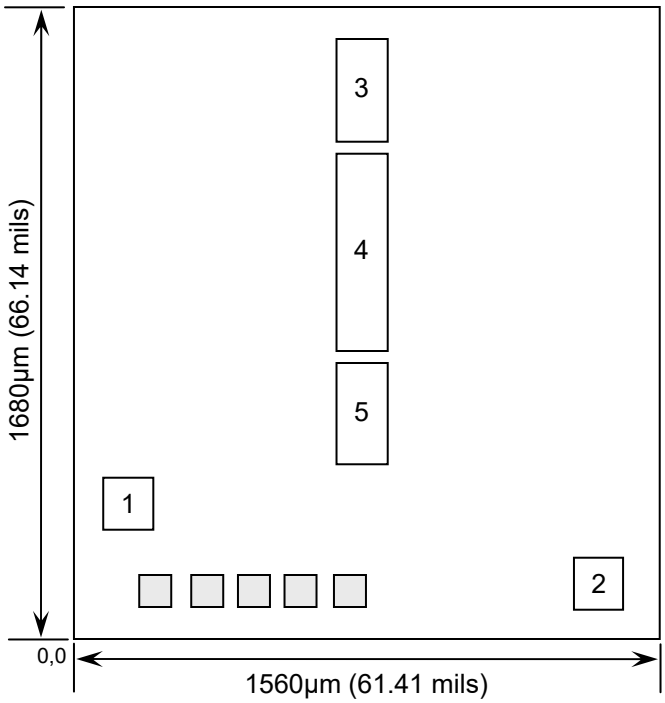
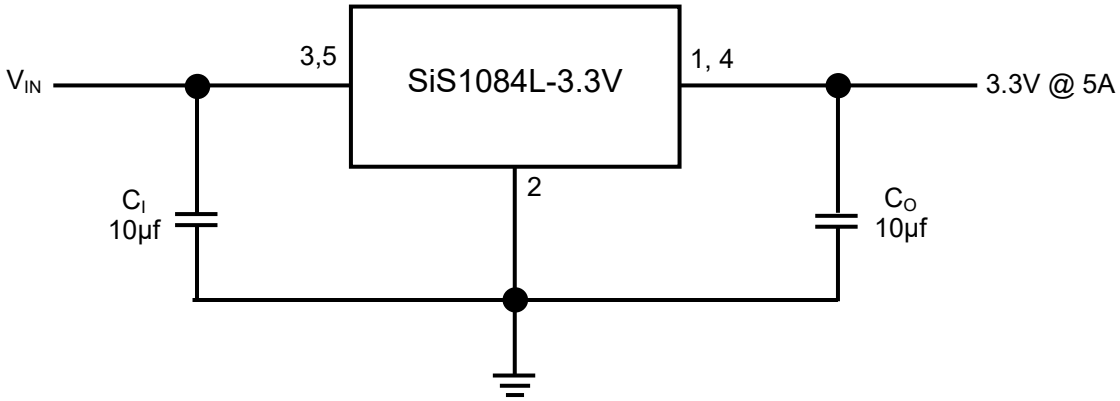
Pad Layout and Functions



PAD	FUNCTION	COORDINATES (µm)	
		X	Y
1	OUTPUT	80	296
2	GND	1346	80
3	INPUT	713	1331
4	OUTPUT	713	771
5	INPUT	713	471
CONNECT CHIP BACK TO OUTPUT			

Typical Application



Application Notes:

C_I is required if the regulator is located an appreciable distance from power supply filter. C_O is not required for stability; however it does improve transient response. For optimum stability and transient response locate C_I C_O as close as possible to the regulator.

The device can operate with up to 12V input voltage supply. This input supply must be well regulated. Additional low ESR input capacitance improves the output noise performance if the input supply is noisy.

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Absolute Maximum Ratings[1] T_J = 25°C unless otherwise stated

PARAMETER	SYMBOL	VALUE	UNIT
Input-to-Output Voltage Differential	V_{DIFF}	12	V
Power Dissipation[2]	P_D	Internally limited	mW
Operating Temperature Range	T_J	-55 to 150	°C
Storage Temperature	T_{STG}	-65 to 150	°C

Recommended Operating Conditions T_J = 25°C unless otherwise stated

PARAMETER	SYMBOL	MIN	MAX	UNIT
Input Voltage	V_{IN}	0	12	V
Output Current	I_{OUT}	-	5	A
Operating Temperature Range	T_J	-55	+125	°C

DC Electrical Characteristics T_J = 25°C unless otherwise specified

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNITS
Reference Voltage	V_{OUT}	I_{OUT} = 10mA, V_{IN} =6.3V	T_J = 25°C	3.270	3.300	3.330	V
		0 ≤ I_{OUT} ≤ $I_{FULL\ LOAD}$, 4.8V ≤ V_{IN} ≤ 10V	T_J = 25°C	3.250	3.300	3.350	
			Full range[3]	3.235	3.300	3.365	
Line Regulation	ΔV_{OUT}	I_{OUT} = 10mA, 4.8V ≤ V_{IN} ≤ 10V	T_J = 25°C	-	-	6	mV
			Full range[3]	-	-	10	
Load Regulation	ΔV_{OUT}	V_{IN} = 5.0V, 0 ≤ I_{OUT} ≤ $I_{FULL\ LOAD}$	T_J = 25°C	-	-	15	mV
			Full range[3]	-	-	20	
Dropout Voltage	V_{IN} - V_{OUT}	ΔV_{REF}, ΔV_{OUT} = 1%, I_{OUT} = 5A	Full range[3]	-	1.3	1.5	V
Minimum Load Current	I_L	V_{IN} = 10V	Full range[3]	-	5	10	mA
Output Current Limit	I_{LIMIT}	V_{IN} = 6.25V	Full range[3]	5.5	-	-	A
Ripple Rejection	RR	I_{OUT} = 5A; V_{IN}=4.25V, f_{RIPPLE} = 120Hz, C_{OUT} =25µF	Full range[3]	60	-	-	dB
Temperature Stability	-		Full range[3]	-	0.5	-	%

1. Operation above the absolute maximum rating may cause device failure. Operation at the absolute maximum ratings, for extended periods, may reduce device reliability. **2.** Results in die form are dependent on die attach and assembly method **3.** −55°C ≤ T_J ≤ 125°C

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