

NPN Transistor Bare Die, MPSA42

Rev 1.0 02/09/17

General purpose high voltage amplifier in bare die form

Complement to PNP MPSA92

Features:

- High Breakdown Voltage
- Low V_{CE(sat)}
- Characterized at temperature extremes
- High Reliability Gold Back Metal
- High Reliability tested grades for Military + Space

Ordering Information:

The following part suffixes apply:

- No suffix MIL-STD-750 /2072 Visual Inspection
- "H" MIL-STD-750 /2072 Visual Inspection+ MIL-PRF-38534 Class H LAT
- "K" MIL-STD-750 /2072 Visual Inspection+ MIL-PRF-38534 Class K LAT

LAT = Lot Acceptance Test.

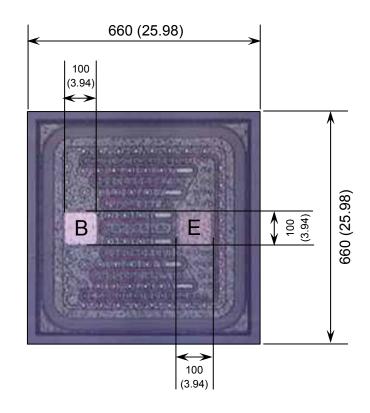
For further information on LAT process flows see below.

www.siliconsupplies.com\quality\bare-die-lot-qualification

Supply Formats:

- Default Die in Waffle Pack (400 per tray capacity)
- Sawn Wafer on Tape Specific request
- Unsawn Wafer Specific request
- With additional electrical selection Specific request
- Sawn as pairs or adjacent pair pick Specific request

Die Dimensions in µm (mils)



E = EMITTER **B** = BASE

DIE BACK = COLLECTOR

Mechanical Specification

Die Size (Excluding Saw Street)	660 x 660 25.98 x 25.98	μm mils	
Base & Emitter Pad Size	100 x 100 3.94 x 3.94	μm mils	
Die Thickness	230 (±20) 9.06 (±0.79)	μm mils	
Top Metal Composition	Al - 2.6μm		
Back Metal Composition	AuAs - 0.9μm		





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Absolute Maximum Ratings T_A = 25°C unless otherwise stated

PARAMETER	SYMBOL	VALUE	UNIT
Collector-Base Voltage	V _{CBO}	300	V
Collector-Emitter Voltage	V _{CEO}	300	V
Emitter-Base Voltage	V _{EBO}	6	V
Collector Current	Ic	500	mA
Junction Temperature	TJ	150	°C
Storage Temperature	T _{stg}	-55 to 150	°C

Electrical Characteristics T_A = 25°C unless otherwise stated

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT		
OFF CHARACTERISTICS								
Collector-Base Breakdown Voltage	V _{(BR)CBO}	I _C = 100μA	300	-	-	V		
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	I _C = 1mA	300	-	-	V		
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	I _E = 100μA	6	-	-	V		
Collector Cut-off Current	I _{CBO}	V _{CB} = 300V	-	-	0.1	μA		
Emitter Cut-off Current	I _{EBO}	V _{EB} = 6V	-	-	0.1	μA		
ON CHARACTERISTICS								
Forward-Current Transfer Ratio	h _{FE}	V _{CE} = 10V, I _C = 1mA	25	-	-	-		
		$V_{CE} = 10V, I_{C} = 10mA$	40	-	-	-		
		$V_{CE} = 30V, I_{C} = 10mA$	40	-	-	-		
Collector-Emitter Saturation Voltage	V _{CE(sat)}	$I_C = 20$ mA, $I_B = 2$ mA	-	-	0.5	V		
Base Saturation Voltage	V _{BE(sat)}	$I_C = 20$ mA, $I_B = 2$ mA	-	-	0.9	V		
SMALL SIGNAL CHARACTERISTICS ¹								
Transition Frequency	f⊤	V _{CE} = 10V, V _{CE} = 20mA, f = 100MHz	50	-	-	MHz		
Output Capacitance	C _{obo}	$V_{CB} = 20V, I_E = 0, f = 1MHz$	-	2.5	-	pF		

Note 1: Not production testing in die form. Characterized by chip design and tested in package LAT.

Typical Electrical Characteristics

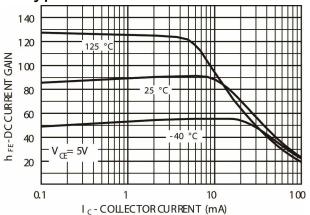


Fig 1 - Typical Pulsed Current Gain versus Collector Current

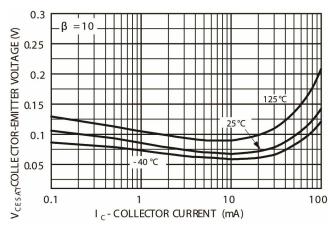


Fig 2 - Collector-Emitter Saturation Voltage versus Collector Current





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Typical Electrical Characteristics (Continued)

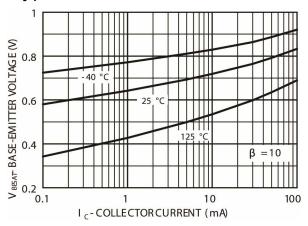


Fig 3 - Base-Emitter Saturation Voltage versus

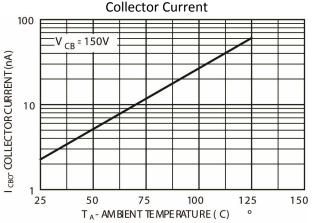


Fig 5 - Collector-Cut-off Current versus Ambient Temperature

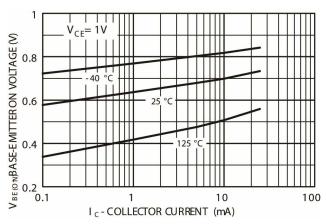


Fig 4 - Base-Emitter ON Voltage versus Collector

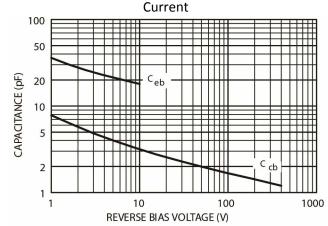


Fig 6 – Collector-Base and Emitter-Base Capacitance versus Reverse Bias Voltage

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