



8 GHz f_T Wideband PNP Chip – BFT977

Silicon PNP Planar RF Transistor in bare die form

Rev 1.0
17/6/25

Description

PNP transistor in unencapsulated chip form. It is primarily intended for use in RF wideband amplifiers, such as in aerial amplifiers, radar systems, oscilloscopes, spectrum analyzers, etc. The transistor features low intermodulation distortion and high power gain; due to its very high transition frequency, it also has excellent wideband properties and low noise up to high frequencies. The device provides equivalent or higher performance than legacy transistors such as BFT92, BFT93, NE97833 or NE97733. Closest available NPN complement is 2SC3356.

Ordering Information

The following part suffixes apply:

- No suffix - MIL-STD-750 /2072 Visual Inspection
- “H” - MIL-STD-750 /2072 Visual Inspection
+ MIL-PRF-38534 Class H LAT
- “K” - MIL-STD-750 /2072 Visual Inspection
+ MIL-PRF-38534 Class K LAT

LAT = Lot Acceptance Test.

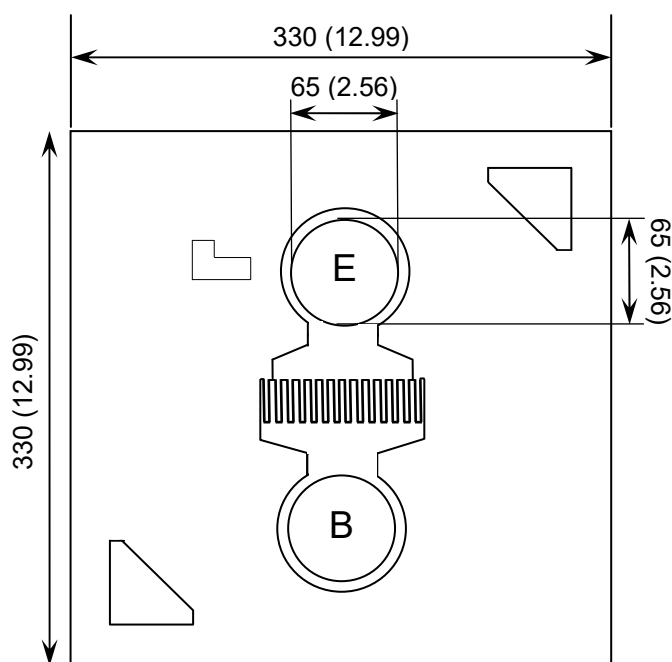
For further information on LAT process flows see below.

www.siliconsupplies.com/quality/bare-die-lot-qualification

Features:

- High Power Gain
- Low Noise
- Wide Transition Frequency

Die Dimensions in μm (mils)



E = EMITTER, B = BASE
CHIP BACKSIDE IS COLLECTOR

Supply Formats:

- Default – Die in Waffle Pack (400 per tray capacity)
- Sawn Wafer on Tape – By specific request
- Unsawn Wafer – By specific request
- With additional electrical selection – Specific request
- Sawn as pairs or adjacent pair pick – Specific request

Mechanical Specification

Die Size (Unsawn)	330 x 330 12.99 x 12.99	μm mils
Base & Emitter Bond Pad Size	65 \varnothing 2.56 \varnothing	μm mils
Die Thickness	180 (± 10) 7.09 (± 0.39)	μm mils
Top Metal Composition	Al	
Back Metal Composition	Au 1 μm	





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Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_{CBO}	Collector-Base Voltage	Open Emitter	-	-20	V
V_{CEO}	Collector-Emitter Voltage	Open Base	-	-12	V
V_{EBO}	Emitter-Base Voltage	Open Collector	-	-3	V
I_C	DC Collector Current	-	-	-50	mA
P_{tot}	Total Power Dissipation	-	-	200	mW
T_{stg}	Storage Temperature	-	-65	150	$^\circ\text{C}$
T_J	Junction Temperature	-	-	150	$^\circ\text{C}$
$R_{TH (J-C)}$	Thermal Resistance (Junction to Case)	Assembled in Micro-X Package	-	85	-

Electrical Characteristics $T_A = 25^\circ\text{C}$

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	SYMBOL
I_{CBO}	Collector Cut-off Current	$I_E = 0$; $V_{CB} = -10\text{V}$	-	-	-0.1	μA
I_{EBO}	Emitter Cut-off Current	$I_C = 0$; $V_{EB} = -1\text{V}$	-	-	-0.1	μA
h_{FE}	DC Current Gain	$I_C = -20\text{mA}$; $V_{CE} = -8\text{V}$	50	-	210	-
f_T	Transition Frequency	$I_C = -20\text{mA}$; $V_{CE} = -8\text{V}$; $f = 1\text{ GHz}$	6	8	-	GHz
$ S_{21e} ^2$	Insertion Power Gain	$I_C = -20\text{mA}$; $V_{CE} = -8\text{V}$; $f = 1\text{ GHz}$	-	12	-	dB
NF	Noise Figure	$I_C = -3\text{mA}$; $V_{CE} = -8\text{V}$; $f = 1\text{ GHz}$	-	1.5	-	dB
C_{re}	Feedback Capacitance	$I_C = -20\text{mA}$; $V_{CE} = -8\text{V}$; $f = 1\text{ MHz}$	-	0.5	1	pF

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