

# 4.5 GHz Wideband PNP Chip - BFQ51

### Silicon PNP Planar RF Transistor in bare die form

Rev 1.1 3/11/17

## Description

PNP transistor in unencapsulated chip form. It is primarily intended for use in RF wideband amplifiers, such as in aerial amplifiers, radar systems, oscilloscopes, spectrum analyzers, etc. The transistor features low intermodulation distortion and high power gain; due to its very high transition frequency, it also has excellent wideband properties and low noise up to high frequencies. NPN complements are BFR92 and BFR92A.

# **Ordering Information**

The following part suffixes apply:

- No suffix MIL-STD-750 /2072 Visual Inspection
- "H" MIL-STD-750 /2072 Visual Inspection+ MIL-PRF-38534 Class H LAT
- "K" MIL-STD-750 /2072 Visual Inspection+ MIL-PRF-38534 Class K LAT

LAT = Lot Acceptance Test.

For further information on LAT process flows see below.

www.siliconsupplies.com\quality\bare-die-lot-qualification

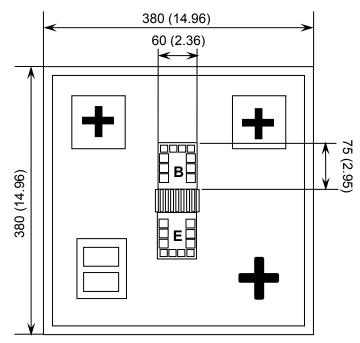
# **Supply Formats:**

- Default Die in Waffle Pack (400 per tray capacity)
- Sawn Wafer on Tape By specific request
- Unsawn Wafer By specific request
- With additional electrical selection Specific request
- Sawn as pairs or adjacent pair pick Specific request

### Features:

- High Power Gain
- Low Noise
- Wide Transition Frequency

# Die Dimensions in µm (mils)



B = BASE, E = EMITTER
CHIP BACKSIDE IS COLLECTOR

# **Mechanical Specification**

Die Size (Unsawn)	380 x 380 14.96 x 14.96	μm mils	
Base & Emitter	60 x 75	μm	
Bond Pad Size	2.36 x 2.95	mils	
Die Thickness	180 (±20) μι 7.08 (±0.78) m		
Top Metal Composition	Au 1.5μm		
Back Metal Composition	Au 0.35μm		





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## Absolute Maximum Ratings T<sub>A</sub> = 25°C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	-	-20	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-15	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	-2	V
I <sub>C</sub>	DC collector current	-	-	-25	mA
P <sub>tot</sub>	total power dissipation	-	-	200	mW
T <sub>stg</sub>	storage temperature	-	-65	150	°C
T <sub>J</sub>	junction temperature	-	-	175	°C

## Electrical Characteristics T<sub>A</sub> = 25°C

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	SYMBOL
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0 ; V <sub>CB</sub> = -10V	-	-	-100	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = -14mA;V <sub>CE</sub> = -10V	20	-	-	
f⊤	transition frequency	$I_{C}$ = -14mA; $V_{CE}$ = -10V f = 300 MHz	-	4.5	-	GHz
G <sub>P</sub>	power gain	$I_{C}$ = -14mA; $V_{CE}$ = -10V f = 500 MHz	-	16	-	dB
NF	noise figure	$I_C$ = -2mA; $V_{CE}$ = -10V f = 500 MHz	-	2.4	-	dB

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