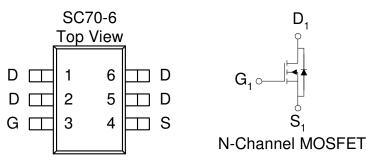
N-Channel 30V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

PRODUCT SUMMARY			
$oxed{V_{DS}(V)} oxed{r_{DS(on)}(\Omega)} oxed{I_D(A)}$		$I_{D}(A)$	
30	$0.035 @ V_{GS} = 4.5 V$	5.5	
	$0.043 @ V_{GS} = 2.5V$	5	

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SC70-6 saves board space
- Fast switching speed
- High performance trench technology



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Maximum	Units		
Drain-Source Voltage		V_{DS}	30	V		
Gate-Source Voltage		V_{GS}	±8			
Continuous Drain Current ^a	$T_A=25^{\circ}C$	Τ_	5.5			
Continuous Drain Current	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	П	4.5	A		
Pulsed Drain Current ^b		I_{DM}	±20			
Continuous Source Current (Diode Conduction) ^a		I_S	1.6	A		
B. B a	$T_A=25^{\circ}C$	P_{D}	1.56	W		
Power Dissipation ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	rD	0.81	VV		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
Maximum Junction-to-Ambient ^a	t <= 5 sec	D	100	0000		
	Steady-State	R_{THJA}	166	[C/W		

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

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SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED)							
Downwator	Symbol	T4 C 142	Limits			TI24	
Parameter		Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	0.3			V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	$I_{ m DSS}$	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
Zero Gate Voltage Drain Current		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10	uA	
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	10			A	
Drain-Source On-Resistance ^A	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 1 \text{ A}$			35	mΩ	
Diani-Source Oil-Resistance		$V_{GS} = 2.5 \text{ V}, I_{D} = 1 \text{ A}$			43		
Forward Tranconductance ^A	${f g}_{ m fs}$	$V_{DS} = 4.5 \text{ V}, I_{D} = 1 \text{ A}$		11.3		S	
Diode Forward Voltage	V_{SD}	$I_S = 1 A, V_{GS} = 0 V$		0.75		V	
Dynamic ^b							
Total Gate Charge	Q_{g}			6			
Gate-Source Charge	Q_{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 1 \text{ A}$		1		nC	
Gate-Drain Charge	Q_{gd}			1			
Turn-On Delay Time	t _{d(on)}			9			
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_L = 15 \Omega, I_D = 1 \text{ A},$		10		, ,,	
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = 4.5 \text{ V}$		40		ns	
Fall-Time	t _f			10			

Notes

a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.

b. Guaranteed by design, not subject to production testing.

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