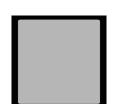


CPW4-1200-S008B Silicon Carbide Schottky Diode Chip Z-REC[®] RECTIFIER

 V_{RRM} = 1200 V I_{F} = 8 A Q_{-} = 37 nC

Features

- 1.2kV Schottky Rectifier
- Zero Reverse Recovery
- Zero Forward Recovery
- High-Frequency Operation
- · Temperature-Independent Switching Behavior
- · Extremely Fast Switching
- Positive Temperature Coefficient on V_E



Chip Outline

| Part Number | Die Size | Anode | Cathode |
|-----------------|-----------------------------|-------|---------|
| CPW4-1200-S008B | 2.00 x 2.00 mm ² | Al | Ni/Ag |

Maximum Ratings

| Symbol | Parameter | Value | Unit | Test Conditions | Note |
|--------------------|--|----------------|------|--|------|
| V_{RRM} | Repetitive Peak Reverse Voltage | 1200 | ٧ | | |
| V _{RSM} | Surge Peak Reverse Voltage | 1300 | ٧ | | |
| V _R | DC Peak Blocking Voltage | 1200 | ٧ | | |
| I _F | Continuous Forward Current | 8 | А | T _J =175°C | 1 |
| I _{FRM} | Repetitive Peak Forward Surge Current | 37.5 25 | А | T_c =25°C, t_p =10 ms, Half Sine Pulse T_c =110°C, t_p =10 ms, Half Sine Pulse | 1 |
| I _{FSM} | Non-Repetitive Forward Surge Current | 64 49.5 | А | T_c =25°C, t_p =10 ms, Half Sine Pulse T_c =110°C, t_p =10 ms, Half Sine Pulse | 1 |
| I _{F,Max} | Non-Repetitive Peak Forward Current | 600 480 | А | T_c =25°C, t_p =10 μ s, Pulse T_c =110°C, t_p =10 μ s, Pulse | |
| T_J , T_{stg} | Operating Junction and Storage Temperature | -55 to +175 | °C | | |
| T _{Proc} | Maximum Processing Temperature | 325 | °C | 10 min. maximum | |

^{1.} Assumes $\rm \textit{R}_{\tiny \theta JC}$ Thermal Resistance of 1.26 $^{\circ}\text{C/W}$ or less



Electrical Characteristics

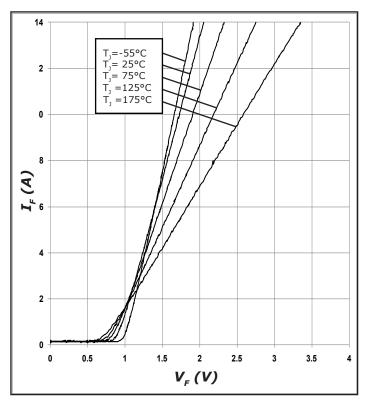
| Symbol | Parameter | Тур. | Max. | Unit | Test Conditions | Note |
|----------------|-------------------------|-----------------|------------|------|--|--------|
| V _F | Forward Voltage | 1.5 2.2 | 1.8 3 | V | I _F = 8 A T _J =25°C I _F = 8 A T _J =175°C | Fig. 1 |
| I _R | Reverse Current | 35 100 | 250 350 | μA | V _R = 1200 V T _J =25°C V _R = 1200 V T _J =175°C | Fig. 2 |
| Q _c | Total Capacitive Charge | 37 | | nC | $V_R = 800 \text{ V, } I_F = 8 \text{ A}$ $di/dt = 200 \text{ A/}\mu\text{s}$ $T_J = 25^{\circ}\text{C}$ | Fig. 3 |
| С | Total Capacitance | 560 37 27 | | pF | V _R = 0 V, T _J = 25°C, f = 1 MHz V _R = 400 V, T _J = 25°C, f = 1 MHz V _R = 800 V, T _J = 25°C, f = 1 MHz | Fig. 4 |

Mechanical Parameters

| recondition i diditionero | | | | | |
|------------------------------|-------------|------|--|--|--|
| Parameter | Тур. | Unit | | | |
| Die Size | 2.00 x 2.00 | mm | | | |
| Anode Pad Size | 1.72 x 1.72 | mm | | | |
| Anode Pad Opening | 1.44 x 1.44 | mm | | | |
| Thickness | 377 ± 10% | μm | | | |
| Wafer Size | 100 | mm | | | |
| Anode Metalization (Al) | 4 | μm | | | |
| Cathode Metalization (Ni/Ag) | 1.8 | μm | | | |
| Frontside Passivation | Polyimide | | | | |



Typical Characteristics





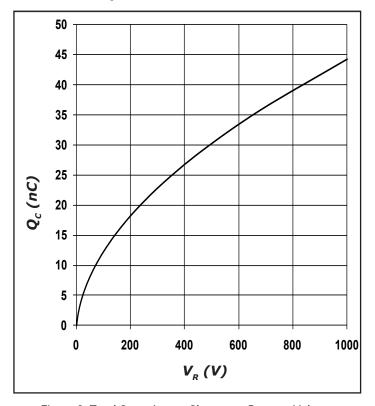


Figure 3. Total Capacitance Charge vs. Reverse Voltage

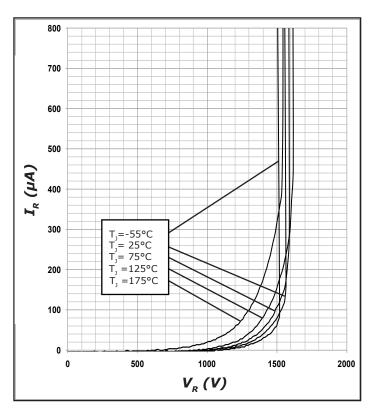


Figure 2. Reverse Characteristics

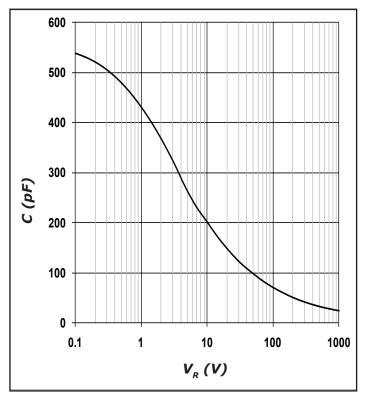
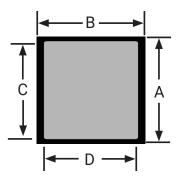


Figure 4. Capacitance vs. Reverse Voltage



Chip Dimensions



| symbol | dimension | | | |
|--------|-----------|-------|--|--|
| | mm | inch | | |
| А | 2.00 | 0.079 | | |
| В | 2.00 | 0.079 | | |
| С | 1.44 | 0.057 | | |
| D | 1.44 | 0.057 | | |

Notes

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Cree representative or from the Product Documentation sections of www.cree.com.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

 This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

Related Links

- Cree SiC Schottky diode portfolio: http://www.cree.com/diodes
- CPW4 Spice models: http://response.cree.com/Request_Diode_model
- SiC MOSFET and diode reference designs: http://response.cree.com/SiC_RefDesigns