

## Positive Fixed 3.3V Output Low Dropout Voltage Regulator in bare die form

# Description

The SiS1084L-3.3V is a positive 3.3V fixed output regulator providing 5A output current with high efficiency. The device accepts input voltages up to 12V and is optimised for smallest die size. Voltage dropout is guaranteed at 1.5V maximum at 5A. This device also features on-chip trimming for current limit + output voltage and includes thermal shutdown for rugged performance.

# Features:

- 12V input capability with optimised die size
- 1.5V dropout voltage maximum et 5A
- Internal current limiting & recence trimming
- Thermal shutdown
- Line & Load Regulation: 20mV maximum
- Full military te npenature range.

Die Qimensions in  $\mu m$  (mils)

# **Ordering Information**

The following part suffixes apply:

- No suffix MIL-STD-883 /2010B Visual Inspection
- "H" MIL-STD-883 /2010B Visual Inspection + MIL-PRF-38534 Class H LAT
- "K" MIL-STD-883 /2010A Visual Inspection (Space)
  + MIL-PRF-38534 Class K\_AT

LAT = Lot Acceptance Test.

For further information on LAT process flows see below.

www.siliconsupplies.com\quality\bare-die-lot-qualification

# Supply Formats:

- Defact Die in Waffle Pack (100 per tray capacity)
- Sawn Wafer on Tape On request
- Un-sawn Wafer On request
- In Metal or Ceramic package On request

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# **Mechanical Specification**

Die Size (Unsawn)	1560 x 1680 61 x 66	μm mils	
Minimum Bond Pad Size	130 x 130 5.11 x 5.11	μm mils	
Die Thickness	350 (±20) 13.78 (±0.79)	µm mils	
Top Metal Composition	Al 1%Si 1.4µm		
Back Metal Composition	Ti/Ni/Ag		

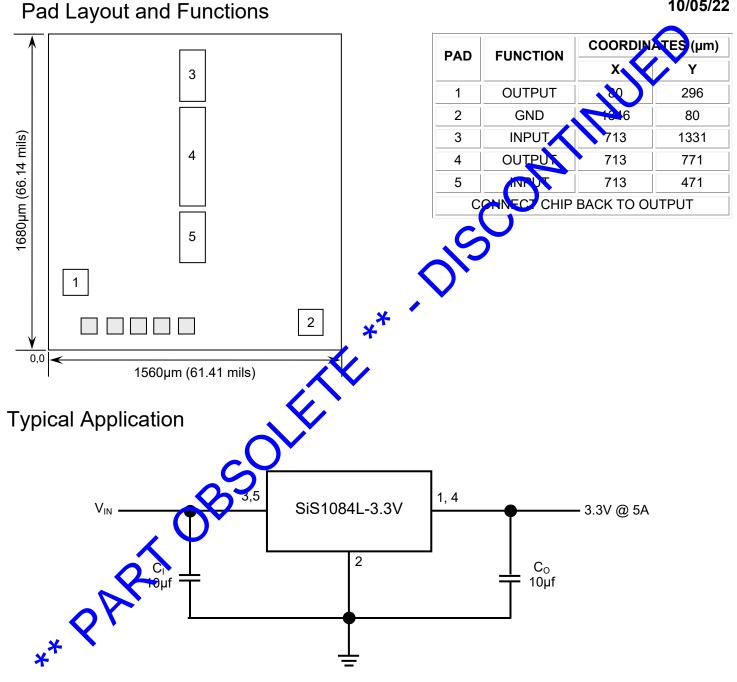


**Rev 1.0** 

10/05/22



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#### Application Notes:

 $C_l$  is required if the regulator is located an appreciable distance from power supply filter.  $C_0$  is not required for stability; however it does improve transient response. For optimum stability and transient response locate  $C_l C_0$  as close as possible to the regulator.

The device can operate with up to 12V input voltage supply. This input supply must be well regulated. Additional low ESR input capacitance improves the output noise performance if the input supply is noisy.





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Absolute Maximum Ratings <sup>1</sup>	$T_{J} = 25^{\circ}C$ unless otherwise stated
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PARAMETER	SYMBOL	VALUE	UNIZ
Input-to-Output Voltage Differential	V <sub>DIFF</sub>	12	N.V.
Power Dissipation <sup>2</sup>	PD	Internally limited	mW
Operating Temperature Range	TJ	-55 to 150	С
Storage Temperature	T <sub>STG</sub>	-65 to 150	°C

# Recommended Operating Conditions T<sub>J</sub> = 25°C unless otherwise stated

PARAMETER	SYMBOL	MIN	NIV X	UNIT
Input Voltage	V <sub>IN</sub>	0	2	V
Output Current	I <sub>OUT</sub>		5	А
Operating Temperature Range	TJ	C55	+125	°C

# DC Electrical Characteristics TJ = 25°C unless otherwise specified

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNITS
Reference Voltage	V <sub>OUT</sub>	$I_{OUT} = 10$ mA, $V_{IN} = 6.3$ V	T <sub>J</sub> = 25°C	3.270	3.300	3.330	V
		$0 \leq I_{OUT} \leq I_{FULL LAD}$	T <sub>J</sub> = 25°C	3.250	3.300	3.350	
		$4.8V \le V_{\rm IN} \le 10V$	Full range <sup>3</sup>	3.235	3.300	3.365	
Line Regulation $\Delta V_{OU}$		$I_{OUT} = 10 \text{mA}$	T <sub>J</sub> = 25°C	-	-	6	mV
	<b>Av</b> 001	4.8V ≤ V <sub>N</sub> ≤ 10V	Full range <sup>3</sup>	-	-	10	
Load Regulation Δ	ΔV <sub>OUT</sub>	$\Delta V_{ave} = 5.0 V,$	T <sub>J</sub> = 25°C	-	-	15	mV
Loud Regulation	$0 \leq I_{OJT} \leq I_{FULL \ LOAD}$	Full range <sup>3</sup>	-	-	20	IIIV	
Dropout Voltage	V <sub>IN</sub> - V <sub>OUT</sub>	REF, ΔV <sub>OUT</sub> = 1%, I <sub>OUT</sub> = 5A	Full range <sup>3</sup>	-	1.3	1.5	V
Minimum Load Current	$\bigcirc$	V <sub>IN</sub> = 10V	Full range <sup>3</sup>	-	5	10	mA
Output Current Limit	ILIMIT	V <sub>IN</sub> = 6.25V	Full range <sup>3</sup>	5.5	-	-	A
Ripple Reject or	RR	$I_{OUT} = 5A; V_{IN} = 4.25V,$ $f_{RIPPLE} = 120Hz,$ $C_{OUT} = 25\mu F$	Full range <sup>3</sup>	60	-	-	dB
Temperature Stability	-		Full range <sup>3</sup>	-	0.5	-	%

**1.** Operation above the absolute maximum rating may cause device failure. Operation at the absolute maximum ratings, for extended periods, may reduce device reliability. **2.** Results in die form are dependent on die attach and assembly method **3.**  $-55^{\circ}C \le T_{J} \le 125^{\circ}C$ 





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