

Negative Fixed 5V Voltage Regulator in bare die form

Rev 1.0 11/15/19

Description

79L05 5V fixed 3-terminal negative voltage regulator delivers up to 100mA output current & is equipped with internal limiting + thermal shutdown features for overload immunity. Implementing this device at point-of-source removes the complexity of single point regulation methods + reduces noise. In replacement of a Zener diode/resistor combination, the device improves output impedance by x2 order of magnitude and delivers lower bias current with lower noise. The 79L05 can also be used with power-pass elements to make high-current voltage regulators.

Ordering Information

The following part suffixes apply:

- No suffix MIL-STD-883 /2010B Visual Inspection
- "H" MIL-STD-883 /2010B Visual Inspection
 + MIL-PRF-38534 Class H LAT
- "K" MIL-STD-883 /2010A Visual Inspection (Space)
 + MIL-PRF-38534 Class K_AT

LAT = Lot Acceptance Test.

For further information on LAT places flows see below.

www.siliconsupplies.com\quality\bare-die-lot-qualification

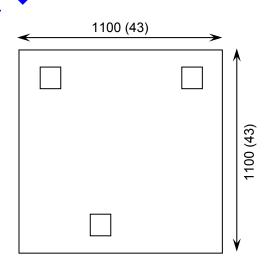
Supply Formats:

- Defaut Die in Waffle Pack (400 per tray capacity)
- Sawn Wafer on Tape On request
- Unsawn Wafer On request
- With Ti/Ni/Ag Back Metal On request
- In Metal or Ceramic package On request

Features:

- ±5% V_{OUT} tolerance over entire temperature range
- 100mA Output Current
- Internal thermal overload processing
- Internal short circuit current imit
- Full Military Temporature Range
- Positive Volta e complement is 78L05

Die Qin ensions in µm (mils)



Mechanical Specification

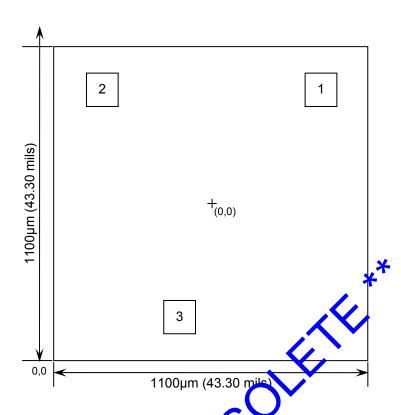
Die Size (Unsawn)	1100 x 1100 43 x 43	μm mils	
Minimum Bond Pad Size	116 x 116 4.57 x 4.57	μm mils	
Die Thickness	280 (±20) 11 (±0.8)		
Top Metal Composition	Al 1%Si 1.4μm		
Back Metal Composition	N/A – Bare Si		





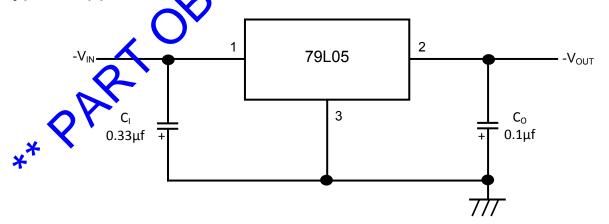
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Pad Layout and Functions



PAD	FUNCTION	ATES (µm)		
ואם	TONOTION	Y	Υ	
1	-V _{IN}	382	397	
2	-V _{CST}	-382	397	
3	SID	-110	-397	
CONNECT CHIP BACK TO VIN				

Typical Application



 C_l is required if the regulator is located an appreciable distance from power supply filter. C_O is not required for stability; however it does improve transient response. For optimum stability and transient response locate C_l C_O as close as possible to the regulator.





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Absolute Maximum Ratings

PARAMETER	SYMBOL	VALUE	UNIZ
Input Voltage	V _{IN}	-30	V,
Power Dissipation ¹	P _D	620	mW/
Operating Temperature Range	-	-55 to 125	C
Maximum Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-65 to 150	°C

Recommended Operating Conditions

PARAMETER	SYMBOL	MIN	NAX	UNIT
Input Voltage	V _{IN}	7	-20	V
Output Current	I _{out}	5	100	mA
Operating Temperature Range	TJ	-55	125	°C

DC Electrical Characteristics V_I = -10V, I_{OUT}=40mA,C_I=0.33µF, C_O=0.1µf, 0°C<T_J<+125°C(unless noted otherwise)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS	
Output Voltage	V _{OUT}	$T_J = 25^{\circ}C, I_O = 40 \text{ pnA}$	-4.80	-5.00	-5.20	V	
		$1 \text{mA} \le I_{\text{OUT}} \le 40 \text{n/A}, -7V \le V_{\text{IN}} \le -20V$	-4.75	-5.00	-5.25		
		$1 \text{mA} \le I_{\text{OUT}} \le 70 \text{mA}, V_{\text{IN}} = -10 \text{V}$	-4.75	-5.00	-5.25		
Line Regulation	Line Regulation ΔV _{OUT}	$-7V \le V_{IN} \le 20V, T_{J} = 25^{\circ}C, I_{O} = 40mA$	-	32	150	mV	
Line Regulation Avol	4 001	$-8V \le V_{IN} \le 20V, T_{J} = 25^{\circ}C, I_{O} = 40mA$	-	26	100		
Load Regulation ΔV _{OU}	ΛVουτ	$1 \text{ mA} \leq 1_{\text{OUT}} \leq 100 \text{mA}, T_{\text{J}} = 25 ^{\circ}\text{C}$	-	15	60	IIIV	
	A V001	1m	-	8	30		
Input Bias Current	l ₂ O	 = 25°C	-	3.8	6.0	mA	
	1B	T _J = 125°C	-	-	5.5		
Input Bias Current		-8V ≤ V _{IN} ≤ -20V	-	-	1.5	mA	
Change		1mA ≤ I _{OUT} ≤ 40mA	-	-	0.1	IIIA	
Output Noise Voltage	e _N	10Hz ≤ f ≤ 100KHz, T _J = 25°C	-	40	-	μV_{RMS}	
Ripple Rejection	RR	f = 120Hz,-8V ≤ V _{IN} ≤ 18V,T _J = 25°C	41	49	-	dB	
Dropou Valtage	V_D	V _{IN} -V _{OUT}	-	1.7	-	V	

^{1.} Value measured in TO-92 package applicable only for DC power dissipation permitted by absolute maximum ratings. Results in die form are dependent on die attach and assembly method.





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