

Positive Fixed 5V Voltage Regulator in bare die form

Description

The 78L05 is a 5V fixed 3-terminal voltage regulator delivering up to 100mA of output current and equipped with internal limiting + thermal shutdown features for overload immunity. Implementing this device at pointof-source removes the complexity of single point regulation methods with reduced noise. Used in replacement of a Zener diode/resistor combination, the device improves output impedance by x2 order of magnitude and delivers lower bias current with lower noise. The 78L05 can also be used with power-pass elements to make high-current voltage regulators.

Ordering Information

The following part suffixes apply:

- No suffix MIL-STD-883 /2010B Visual Inspection
- "H" MIL-STD-883 /2010B Visual Inspection + MIL-PRF-38534 Class H LAT
- "K" MIL-STD-883 /2010A Visual Inspection (Space)
 + MIL-PRF-38534 Class K_AT

LAT = Lot Acceptance Test.

For further information on LAT process flows see below.

www.siliconsupplies.com\quality\bare-die-lot-qualification

Supply Formats:

- Defact Die in Waffle Pack (400 per tray capacity)
- Sawn Wafer on Tape On request
- Unsawn Wafer On request
- With Ti/Ni/Ag Back Metal On request
- In Metal or Ceramic package On request

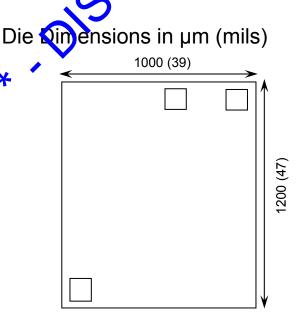
Features:

• ±5% V_{OUT} tolerance over entire temperature range

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- 100mA Output Current
- Internal thermal overload protection
- Internal short circuit current hmit
- Full Military Temperature Range
- Negative Voltage complement is 79L05



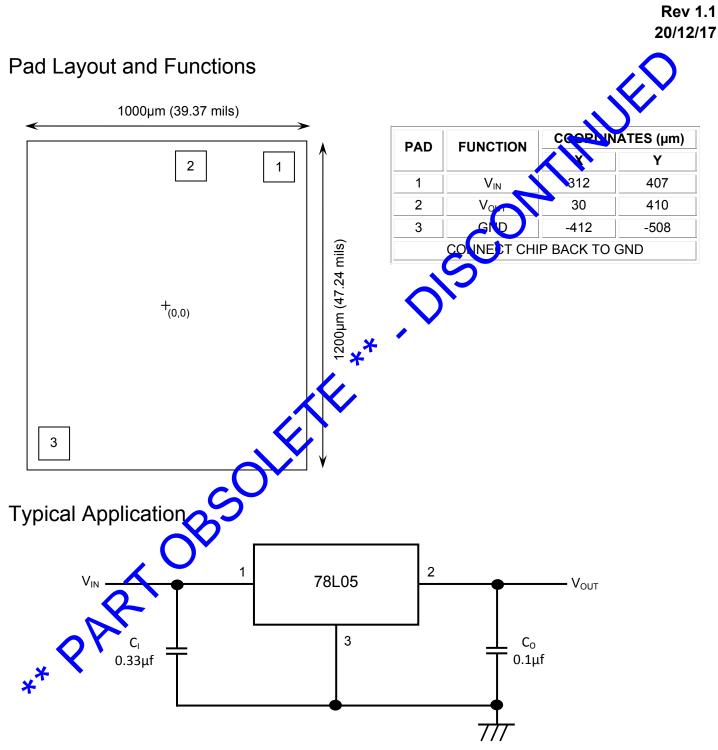
Mechanical Specification

Die Size (Unsawn)	1000 x 1200	μm	
	39.37 x 47.24	mils	
Minimum Bond Pad Size	110 x 105 4.33 x 4.13	µm mils	
Die Thickness	280 (±20) 11 (±0.8)	µm mils	
Top Metal Composition	Al 1%Si 1.4µm		
Back Metal Composition	N/A – Bare Si		



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 C_l is required if the regulator is located an appreciable distance from power supply filter. C_o is not required for stability; however it does improve transient response. For optimum stability and transient response locate $C_l C_o$ as close as possible to the regulator.

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Absolute Maximum Ratings

PARAMETER	SYMBOL	VALUE	UNIT
Input Voltage	V _{IN}	30	V.
Power Dissipation ¹	PD	620	nW
Operating Temperature Range	-	-55 to 125	Ċ
Maximum Junction Temperature	TJ	150	°C
Storage Temperature	T _{STG}	-65 to 150	°C

Recommened Operating Conditions

PARAMETER	SYMBOL	MIN	MAX	UNIT
Input Voltage	V _{IN}		20	V
Output Current	I _{оит}	5	100	mA
Operating Temperature Range	Tj	-55	125	°C

DC Electrical Characteristics, VI = 10V, IOUT=40mA, CI=0.33µF, CO=0.1µf, 0°C<TJ<+125°C(unless noted otherwise)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Output Voltage V _{OUT}		$T_{\rm J} = 25^{\circ} C, I_{\rm O} = 40 m A$	4.80	5.00	5.20	V
	V_{OUT}	$1\text{mA} \le I_{\text{OUT}} \le 40\text{m}$ A, $7\text{V} \le \text{V}_{\text{IN}} \le 20\text{V}$	4.75	5.00	5.25	
		$1 \text{mA} \le I_{OUT} \le 70 \text{mA}, V_{IN} = 10 \text{V}$	4.75	.75 5.00 5.25		
Line Regulation	ΔV _{OUT}	$7V \le V_{IN} \le 20V$, $T_J = 25^{\circ}C$, $I_O = 40mA$	-	32	150	
	A V 001	$8V \leq V_{IN} \leq 20V$, $T_J = 25^{\circ}C$, $I_O = 40mA$	-	26	100	mV
Load Regulation	Load Regulation ΔV_{OUT}	$1 \text{ nA} \ge I_{OUT} \le 100 \text{ mA}, \text{ T}_{J} = 25^{\circ}\text{C}$	-	15	60	
		$10^{10} \le I_{OUT} \le 40$ mA, T _J = 25°C	-	8	30	
Input Bias Current	T = 25°C	-	3.8	6.0	mA	
		T _J = 125°C	-	-	5.5	
Input Bias Current		$8V \le V_{IN} \le 20V$	-	-	1.5	mA
Change	<u> </u>	$1mA \le I_{OUT} \le 40mA$	-	-	0.1	
Output Noise Voltage	e _N	10Hz ≤ f ≤ 100KHz, T _A = 25°C	-	42	-	μV _{RMS}
Ripple Rejection	RR	$f = 120Hz, 8V \le V_{IN} \le 18V, T_J = 25^{\circ}C$	41	49	-	dB
Dropou Voltage	VD	V _{IN} -V _{OUT}	-	1.7	-	V

1. Value measured in TO-92 package applicable only for DC power dissipation permitted by absolute maximum ratings. Results in die form are dependent on die attach and assembly method.



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